



PATENT

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

Applicants: Mun et al.

Serial No.: 10/699,438

Filed: October 31, 2003

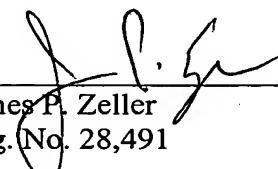
For: SILICON WAFERS AND  
METHOD OF FABRICATING THE  
SAME

Group Art Unit: 2822

Examiner: Maria F. Guerrero

I hereby certify that this paper (or fee) is being deposited with the United States Postal Service, first class postage prepaid, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

September 13, 2005

  
James P. Zeller  
Reg. No. 28,491

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants, through their undersigned attorney, submit the U.S. patents identified on the accompanying Form PTO-1449 for consideration by the examiner, pursuant to the provisions of 37 C.F.R. § 1.97(c).

Out check in the amount of \$180 to cover the fee set forth in 37 C.F.R. § 1.17(p) is submitted herewith.

Entry and consideration of the submitted documents is solicited.

Respectfully submitted,

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By: 

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09/21/2005 EAVALEH1 000000038 10699438

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Form PTO-1449 (Mod. 07/01/2003)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 29936/39510	Serial No. 10/699,438
<b>INFORMATION DISCLOSURE STATEMENT</b>		Applicant Mun et al.	Filing Date 10/31/03
			Group 2822

U.S. PATENT DOCUMENTS							
*Examiner Initials		Document Number	Issue Date	Name	Class	Subclass	Filing Date if Appropriate
		5,502,010	03/26/96	Nadahara et al.	437	247	
		5,502,331	03/26/96	Inoue et al.	257	617	
		6,485,807 B1	11/26/02	Park	428	64.1	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**


Examiner	Date Considered
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	